## A METHOD AND SYSTEM FOR IMPROVED ATE TIMING CALIBRATION AT A DEVICE UNDER TEST

## ABSTRACT OF THE DISCLOSURE

A method for improved ATE (automatic test equipment) timing calibration at a DUT (device under test). The method includes step of accessing a DUT component using an ATE component and performing physical calibration on a first portion of signal pathways coupling the ATE component to the DUT component. A simulation based calibration is performed on a second portion of signal pathways coupling the ATE component to the DUT component.

The physical calibration results are combined with simulation based calibration results to calibrate timing propagation delay between the ATE component and the DUT component.

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